

**Notice of Allowability**

Application No.	Applicant(s)
10/092,964	TSUCHIDA, SHINICHI
Examiner	Art Unit
Brenda Pham	2664

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTO-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1.  This communication is responsive to 03/05/02.
2.  The allowed claim(s) is/are 3-5,7-8, 11, renumbering as 1-6, respectively.
3.  Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a)  All
  - b)  Some\*
  - c)  None of the:
  1.  Certified copies of the priority documents have been received.
  2.  Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3.  Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.  
**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

4.  A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5.  CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.
  - (a)  including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached
    - 1)  hereto or 2)  to Paper No./Mail Date \_\_\_\_\_.
  - (b)  including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6.  DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

**Attachment(s)**

1.  Notice of References Cited (PTO-892)
2.  Notice of Draftsperson's Patent Drawing Review (PTO-948)
3.  Information Disclosure Statements (PTO-1449 or PTO/SB/08),  
Paper No./Mail Date 03/05/02
4.  Examiner's Comment Regarding Requirement for Deposit  
of Biological Material
5.  Notice of Informal Patent Application (PTO-152)
6.  Interview Summary (PTO-413),  
Paper No./Mail Date \_\_\_\_\_.
7.  Examiner's Amendment/Comment
8.  Examiner's Statement of Reasons for Allowance
9.  Other \_\_\_\_\_.

*Brenda Pham*

*11/21/05*

**EXAMINER'S AMENDMENT**

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Mr. Dexter Chang on November 21, 2005.

2. The application has been amended as follows:

Claims 1, 2, 6, 9 and 10 are canceled.

Claim 3 is deleted in entirely and is replace it with the following:

"A multiplexing apparatus, comprising: a time division switch that multiplexes data inputted and outputs the multiplexed data to a selected circuit, and a memory unit that stores real control data for controlling actual connection operations of the time division switch, and virtual control data for controlling virtual connection operations, wherein the memory unit assigns an address to every terminal unit that provides data and every circuit that outputs the multiplexed data, and assigns a virtual address for virtually connecting the address of the terminal unit and the address of the circuit."

Claim 5 is deleted in entirely and is replace it with the following:

“A multiplexing apparatus, comprising: a time division switch that multiplexes data inputted and outputs the multiplexed data to a selected circuit, and a memory unit that stores real control data for controlling actual connection operations of the time division switch, and virtual control data for controlling virtual connection operations, wherein the memory unit detects real control data that is virtually connected by using the virtual control data when controlling connection operations of the time division switch.”

Claim 11 is deleted in entirely and is replace it with the following:

“A network, comprising a multiplexing apparatus which includes:  
a time division switch that multiplexes data inputted and outputs the multiplexed data to a selected circuit, and a memory unit which stores therein real control data for controlling actual connection operations of the time division switch, and virtual control data for controlling virtual connection operations, wherein the memory unit assigns an address to each terminal unit that provides data and every circuit that outputs the multiplexed data, and assigns an address to a virtual transmission path that virtually connects the address of the terminal unit and the address of the circuit.”

## REASONS FOR ALLOWANCE

3. Claims 3-5, 7-8 and 11 are allowed over prior art.
4. The following is an examiner's statement of reasons for allowance: the prior made of record fails to teach or fairly suggests in combination wherein the memory unit assigns an address to every terminal unit that provides data and every circuit that outputs the multiplexed data, and assigns a virtual address for virtually connection the address of the terminal unit and the address of the circuit.

The prior art further fails to teach in combination detecting the address of the terminal unit and the address of the circuit, both having been connected, by deleting the virtual address.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

5. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Mori (US 5,434,857) discloses address control memory includes a reading memory for storing data to an address which is a respective position of the time-division multiplexed data in time slots given when the time-division multiplexed data is read from the data memory as an output data and the data is a respective position of the time-

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division data in the time slots given when the time-division data multiplexed data is written into the data memory as input data.

### **Conclusion**

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Brenda Pham whose telephone number is (571) 272-3135. The examiner can normally be reached on Monday-Friday from 9:00 to 5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Wellington Chin, can be reached on (571) 272-3134.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Group receptionist whose telephone number is (571) 272-2600.

November 21, 2005

Brenda Pham

*Brenda Pham*